

Cisco Support

## SUPER RANGE 2

Powerful Range and Throughput Performance for Wi-Fi Networks





			CARI	NFORMAT	ION				
Chipset				Atheros, 4th Generation, AR52					
adio Opera	ition			IEEE 802.11b/g, 2.4GF					
iterface							32-bit m	ni-PCI Type	
peration V								3.3V	
ntenna Por							u.fl (main), Mi		
mperature	e Range			(D.A. )4/DAD	150 0014 0	TI/ID E	000.4 644	-40C to +8	
ecurity							on, 802.1x, 64/		
ta Rates	W. III 6			61	мбрѕ, 9Мбр		Mbps, 36Mbps,		
HS Comp	Width Support						MHz / 10MHz /		
пъ сопр	liance								
			DECIII AT	RY INFOR	MATTON				
ralace Ma	dular Approval	c	REGULA	KITNIOK	MATION	FCC F	art 15.247, CE	100mW limit	
reless ino	uulai Appilovai	5				100 F	ait 13.247, CL	TOOIIIW IIIIII	
		RADIO OPE	RATING FREQUE	V 2412-24	62 MHz (2	312-2732 MH	[ <b>7</b> *]		
	TX SPF	IFICATIONS	KATING I KEQUE		OZ MIE (Z		CIFICATIONS		
	DataRate	TX Power	Tolerance			DataRate	Sensitivity	Tolerance	
	1Mbps	26 dBm	+/-1dB		Δ	1Mbps	-97 dBm	+/-1dB	
Ξ	2Mbps	26 dBm	+/-1dB		크	2Mbps	-96 dBm	+/-1dB	
2	5.5Mbps	26 dBm	+/-1dB		2.	5.5Mbps	-95 dBm	+/-1dB	
802.11b	11Mbps	26 dBm	+/-1dB		802.11b	11Mbps	-92 dBm	+/-1dB	
	11111003	120 abiii	17 IUD			ТТПОРЗ	JZ dBiii	[17 IGB	
	6Mbps	26 dBm	+/-1dB			6Mbps	-94 dBm	+/-1dB	
802.11g OFDM	9Mbps	26 dBm	+/-1dB		Σ	9Mbps	-93 dBm	+/-1dB	
윤	12Mbps	26 dBm	+/-1dB		æ	12Mbps	-91 dBm	+/-1dB	
0	18Mbps	26 dBm	+/-1dB		0	18Mbps	-90 dBm	+/-1dB	
19	24Mbps	26 dBm	+/-1dB		802.11g OFDM	24Mbps	-86 dBm	+/-1dB	
=	36Mbps	24 dBm	+/-1dB			36Mbps	-83 dBm	+/-1dB	
07	48Mbps	22 dBm	+/-1dB			48Mbps	-77 dBm	+/-1dB	
œ	54Mbps	21 dBm	+/-1dB		<b>∞</b>	54Mbps	-74 dBm	+/-1dB	
	рэнчира	121 abiii	17 Idb			рэтпорз	7 T UDIII	17 Tub	
	ADJUSTAB	LE CHANNEL	SIZE SUPPORT (	crease Cha	nnel Capa	citv or Increa	se Throughpu	t)	
			10MHz				40MHz (Turbo)		
		'				•	•		
			CURRENT CON	MPTION I	NFORMATI	ON			
	TX CURREN	T CONSUMPT	ION			RX CURREN	T CONSUMPTI	ON	
	DataRate	Current	Tolerance			DataRate	Sensitivity	Tolerance	
9	1Mbps	1.10 A	+/-100mA		802.11b	1Mbps	350 mA	+/-100mA	
푸	2Mbps	1.10 A	+/-100mA			2Mbps	350 mA	+/-100m/	
802.11b	5.5Mbps	1.10 A	+/-100mA		22	5.5Mbps	350 mA	+/-100mA	
- œ	11Mbps	1.10 A	+/-100mA		8	11Mbps	350 mA	+/-100mA	
	Land	T				Lavar			
-	6Mbps	1.10 A	+/-100mA		_	6Mbps	350 mA	+/-100mA	
á	9Mbps	1.10 A	+/-100mA		á	9Mbps	350 mA	+/-100mA	
802.11g OFDM	12Mbps	1.10 A	+/-100mA		802.11g OFDM	12Mbps	350 mA	+/-100mA	
9	18Mbps	1.10 A	+/-100mA			18Mbps	350 mA	+/-100mA	
ij	24Mbps	1.10 A	+/-100mA			24Mbps	350 mA	+/-100m <i>A</i>	
	36Mbps	1.00 A	+/-100mA			36Mbps	350 mA	+/-100mA	
80	48Mbps	0.90 A	+/-100mA			48Mbps	350 mA	+/-100mA	
	54Mbps	0.80 A	+/-100mA			54Mbps	350 mA	+/-100mA	
			RANG	PERFORM <i>A</i>	ANCE				
	ndoor (Antenna Dependent):						L	lp to 200met	
			1					Over 50	
	itenna Depende	ent):							
		ent):							
utdoor (An		•	DRIVE	INFORMA	TION		VIFI, WindowsX		
		ent):	- DDTV	TNEORNA	TION				

CCX 4.0 Supported Driver/Utility also available from Ubiquiti
For help with MADWIFI or other Special Driver Support, Please e-mail support@ubnt.com

## **MINI-PCI INTERFACE PINOUT**

#	InUse	Pin Name	Description	#	InUse	Pin Name	Description
1	X	TIP	1 Conductor, local loop wire pair	63	YES	3.3V	3.3 V Supply voltage
2	X	RING	1 Conductor, local loop wire pair	64	YES	FRAME#	Indicates Bulk Transfer
3	X	8PMJ-3	Pin 3 of optional 8-pin modular jack	65	YES	CLKRUN#	Stops clock on certain mobile PCI
4	X	8PMJ-1	Pin 1 of optional 8-pin modular jack	66	YES	TRDY#	Target Ready
5	X	8PMJ-6	Pin 6 of optional 8-pin modular jack	67	YES	SERR#	Catastrophic system error
6	X	8PMJ-2	Pin 2 of optional 8-pin modular jack	68	YES	STOP#	Target wishes to end transfer
7	X	8PMJ-7	Pin 7 of optional 8-pin modular jack	69	YES	GROUND	Ground
8	X	8PMJ-4	Pin 4 of optional 8-pin modular jack	70	YES	3.3V	3.3 V Supply voltage
9	X	8PMJ-8	Pin 8 of optional 8-pin modular jack	71	YES	PERR#	Indicates Parity Error
10	X	8PMJ-5	Pin 5 of optional 8-pin modular jack	72	YES	DEVSEL#	PCI Device Select
11	X	LED1_GRNP	Interface for external LEDs	73	YES	C/BE[1]#	Byte Enable
12	X	LED2_YELP	Interface for external LEDs	74	YES	GROUND	Ground
13	X	LED1_GRNN	RF Silent input	75	YES	AD[14]	Multiplexed Address/Data Bus
14	X	LED2_YELN	Interface for external LEDs	76	YES	AD[15]	Multiplexed Address/Data Bus
15	YES	CHSGND	Chassis Ground	77	YES	GROUND	Ground
16	X	RESERVED	-	78	YES	AD[13]	Multiplexed Address/Data Bus
17	X	INTB#	Interrupt Request B	79	YES	AD[12]	Multiplexed Address/Data Bus
18	X	5V	5 V Supply voltage	80	YES	AD[11]	Multiplexed Address/Data Bus
19	YES	3.3V	3.3 V Supply voltage	81	YES	AD[10]	Multiplexed Address/Data Bus
20	YES	INTA#	Interrupt Request A	82	YES	GROUND	Ground
21	X	RESERVED	-	83	YES	GROUND	Ground
22	X	RESERVED	-	84	YES	AD[09]	Multiplexed Address/Data Bus
23	YES	GROUND	Ground	85	YES	AD[08]	Multiplexed Address/Data Bus
24	YES	3.3VAUX	3.3 V supply-uninterrupted	86	YES	C/BE[0]#	Byte Enable
25	YES	CLK	PCI Clock	87	YES	AD[07]	Multiplexed Address/Data Bus
26	YES	RST#	PCI Reset	88	YES	3.3V	3.3 V Supply voltage
27	YES	GROUND	Ground	89	YES	3.3V	3.3 V Supply
28	YES	3.3V	3.3 V Supply voltage	90	YES	AD[06]	Multiplexed Address/Data Bus
29	YES	REO#	PCI Bus Request	91	YES	AD[05]	Multiplexed Address/Data Bus
30	YES	GNT#	PCI Bus Grant	92	YES	AD[04]	Multiplexed Address/Data Bus
31	YES	3.3V	3.3 V Supply voltage	93	X	RESERVED	- With pleased Address/ Data Bus
32	YES	GROUND	Ground	94	YES	AD[02]	Multiplexed Address/Data Bus
33	YES	AD[31]	Multiplexed Address/Data Bus	95	YES	AD[03]	Multiplexed Address/Data Bus
34	X	PME#	Power Management Event	96	YES	AD[00]	Multiplexed Address/Data Bus
35	YES	AD[29]	Multiplexed Address/Data Bus	97	X	5V	5 V Supply voltage
36	X	RESERVED	- Trustipiezed Fideress/Butta Bus	98	X	RESERVED_WIP5	- Supply voltage
37	YES	GROUND	Ground	99	YES	AD[01]	Multiplexed Address/Data Bus
38	YES	AD[30]	Multiplexed Address/Data Bus	100	X	RESERVED_WIP5	-
39	YES	AD[27]	Multiplexed Address/Data Bus	101	YES	GROUND	Ground
40	YES	3.3V	3.3 V Supply voltage	102	YES	GROUND	Ground
41	YES	AD[25]	Multiplexed Address/Data Bus	103	X	AC_SYNC	AC97 Sync
42	YES	AD[28]	Multiplexed Address/Data Bus	104	X	M66EN	Enables 66 MHz PCI bus
43	YES	RESERVED	-	105	X	AC_SDATA_IN	AC97 Data Input
44	YES	AD[26]	Multiplexed	106	X	AC SDATA OUT	AC97 Data Output
45	YES	C/BE[3]#	Byte Enable	107	X	AC_BIT_CLK	AC97 Bit Clock
46	YES	AD[24]	Multiplexed Address/Data Bus	108	X	AC_CODEC_ID0#	Identifier for AC97 CODEC
47	YES	AD[23]	Multiplexed Address/Data Bus	109	X	AC_CODEC_ID1#	Identifier for AC97 CODEC
48	YES	IDSEL	Initialization Device Select	110	X	AC_RESET#	AC97 Reset
49	YES	GROUND	Ground	111	X	MOD_AUDIO_MON	Modern Audio Monitor
50	YES	GROUND	Ground	112	X	RESERVED	-
51	YES	AD[21]	Multiplexed Address/Data Bus	113	YES	AUDIO_GND	Analog Ground for line-level audio
52	YES	AD[22]	Multiplexed Address/Data Bus	114	X	GROUND	Ground
53	YES	AD[19]	Multiplexed Address/Data Bus	115	X	SYS_AUDIO_OUT	Telephone Audio Out
54	YES	AD[20]	Multiplexed Address/Data Bus	116	X	SYS_AUDIO_IN	Telephone Audio In
55	YES	GROUND	Ground	117	X	SYS_AUDIO_OUTG	Analog Ground for telephone audio
56	YES	PAR	Parity Bit	118	X	SYS_AUDIO_IN_G	Analog Ground for telephone audio
57	YES	AD[17]	Multiplexed Address/Data Bus	119	X	AUDIO_GND	Analog Ground for line-level audio
58	YES	AD[17] AD[18]	Multiplexed Address/Data Bus	120	YES	AUDIO_GND	Analog Ground for line-level audio
59	YES	C/BE[2]#	Byte Enable	120	X	RESERVED	- Amazog Ground for fille-level audio
60	YES	AD[16]	Multiplexed Address/Data Bus	121	X	MPCIACT#	MiniPCI Function Active
	YES	IRDY#	Initiator Ready	123			5V Analog
61					X	VCC5VA	
62	YES	Ground	Ground	124	X	3.3VAUX	3.3 V supply-uninterrupted